

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Summary report

Venue: 305

Date: 26/11/2022

Department of EEE organized a Online Technical Seminar on **"VLSI LAYOUT DESIGN"** from the research domain of VLSI under IEEE & IEI Student chapter for III EEE students on 26/11/2022. Mr. Arun Mondal, Senior Layout Design Engineer, Samsung, Bangalore acted as resource person. In total 61 students attended from III EEE.

	
Banner of the event	Presenting Latest Technologies in VLSI
	
Students participating in seminar	Students participating in seminar

Coordinator

IEI faculty Advisor

Convener

Mr. V Matthew
Assistant Professor

Mrs. R Jaya Lakshmi
Assistant Professor

Dr. Y. Rajendra Babu
Prof. & HOD